



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Michael Lamson  
Serial No. 09/482,332  
Filed: 01/14/2000  
For: SYSTEM FOR MODELING AN ELECTRONIC STRUCTURE, AND  
METHOD OF OPERATION

Art Unit 2128  
Examiner Fred Ferris III  
TI-28063

RECEIVED

AMENDMENT

JUN 01 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Technology Center 2100

Sir:

Responsive to the Final Rejection of 01/26/2004, please amend the application as follows:

Cancel claims 4-8.

Amend claim 1 as follows:

1. (Amended) A system for modeling a package assembly for an integrated circuit device

[electronic structure], comprising:

an input data generator operable to generate input data comprising information describing  
a plurality of characteristics of a package assembly [structure] to be modeled, wherein  
said plurality of characteristics comprise a plurality of segments, subdivisions, and  
compositions;

a segmentation generator, coupled to said input generator, operable to select the most  
effective segments of said package, [structure to be modeled] to organize the sequence  
of said segments, to store the data related to said sequenced segments into a segment  
file, and to convert said segment file into a format suitable for electrical analysis;

an analysis generator, coupled to said segmentation generator, operable to electrically  
analyze said converted segment file in a plurality of calculation programs, executed in